

- Implementation of multiport communication receiver calibration algorithms on FPGA
- System-level modeling of multiport communication receivers
- Broadband microwave design in 65nm standard CMOS technology
 - Broadband power dividers
 - Broadband quadrature couplers
 - Broadband power/video detectors
- High speed analog circuit design in 65nm standard CMOS technology
 - Wideband amplifier design
- 60 GHz VCO design in 65nm standard CMOS technology

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